



PROCKET NO: 240416US0DIV

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
MASAKAZU KANECHIKA, ET AL. : EXAMINER:  
SERIAL NO: 10/618,085 :  
FILED: JULY 14, 2003 : GROUP ART UNIT: 2812  
FOR: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR DEVICE

ATTORNEY DECLARATION

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

I, J. Derek Mason, Ph.D., attorney of record in the above-identified application, declare the attached to be a true and accurate copy of the Specification filed July 14, 2003, which is a divisional application of the parent application Serial Number 09/420,524, filed October 18, 1999.

The undersigned declares further that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under

Application No. 10/618,085

Reply to Notice to File Missing Parts of October 10, 2003

Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



Very truly yours,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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